

Fig. 1

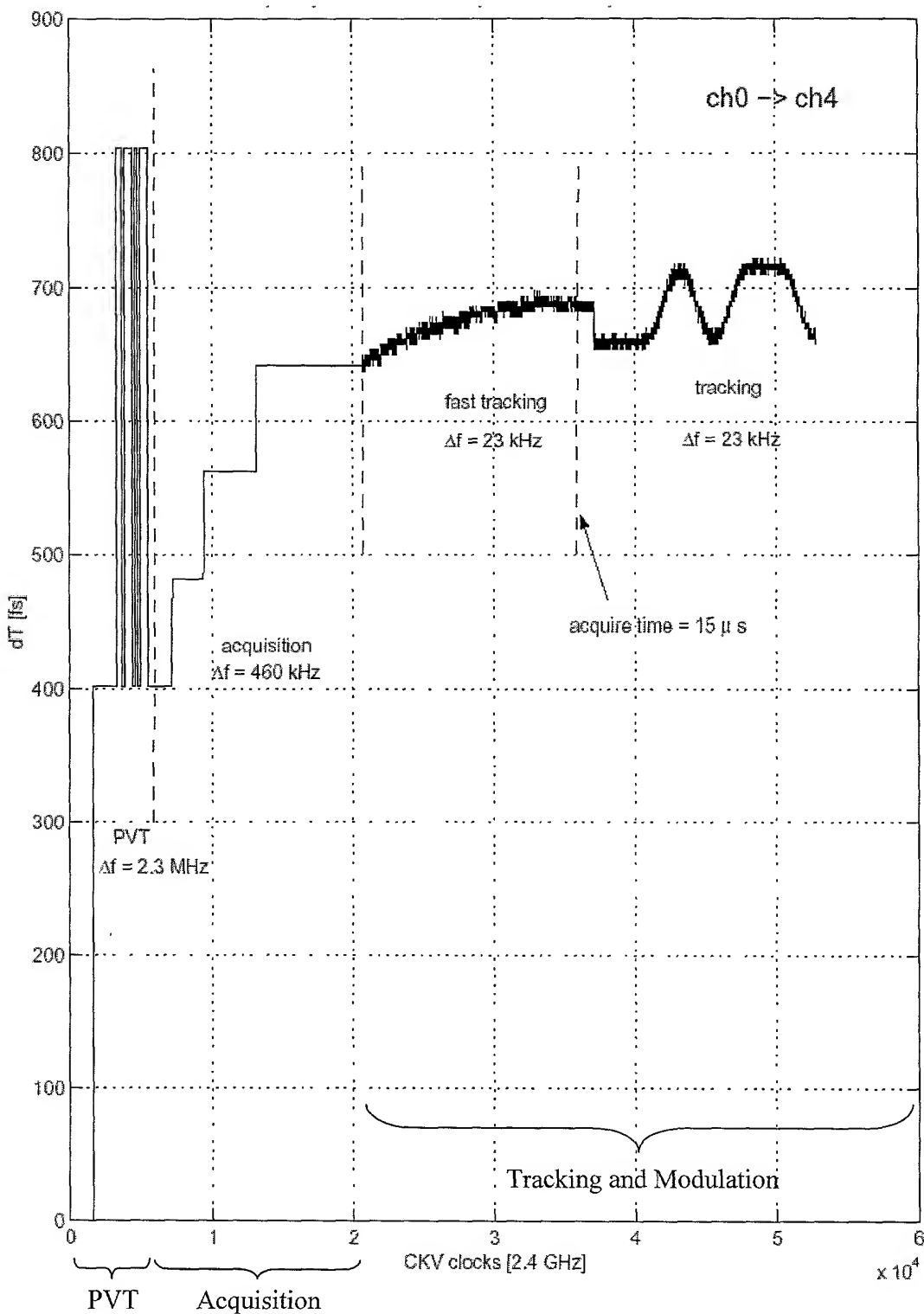


Fig. 2

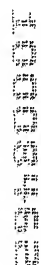
[illegible]

Fig. 4a

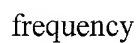


Fig. 4b

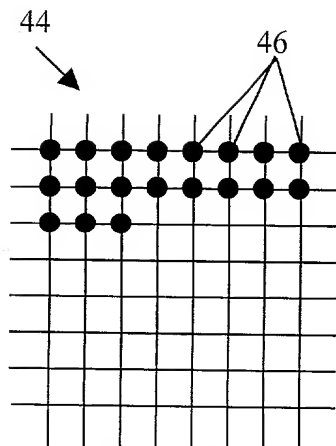


Fig. 5a

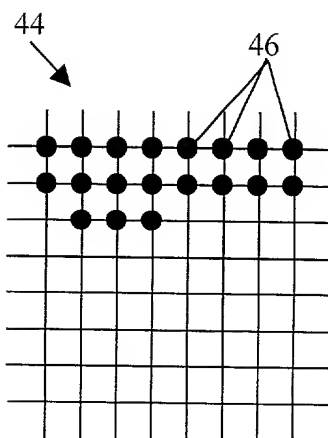


Fig. 5b

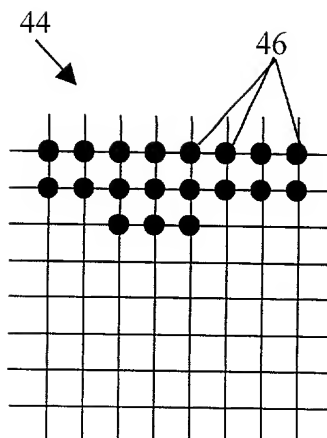


Fig. 5c

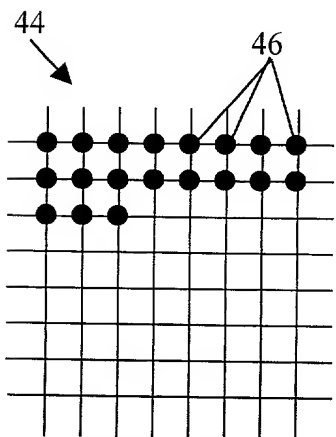


Fig. 6a

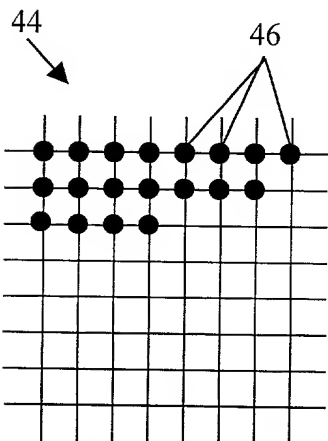


Fig. 6b

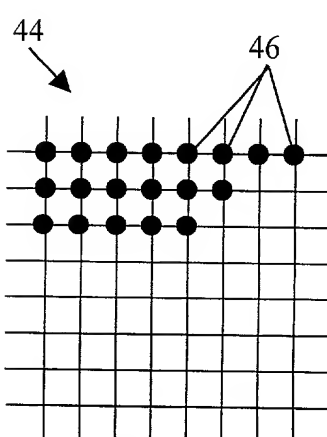


Fig. 6c

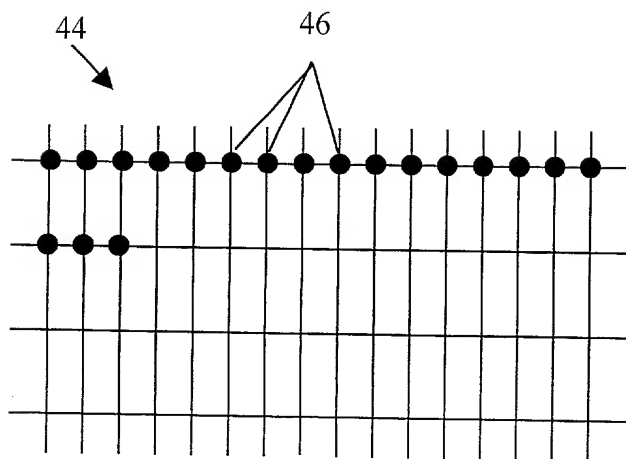


Fig. 7a

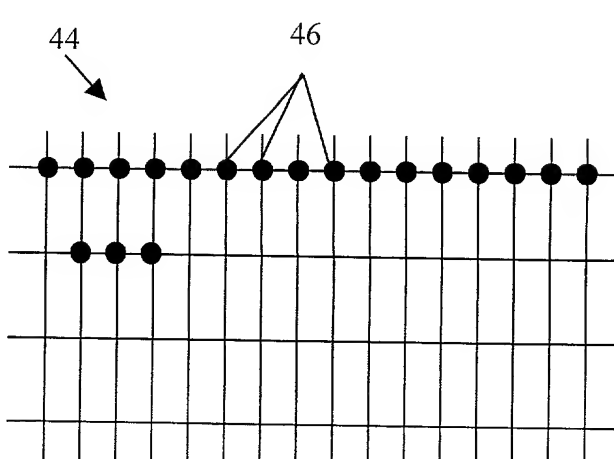


Fig. 7b

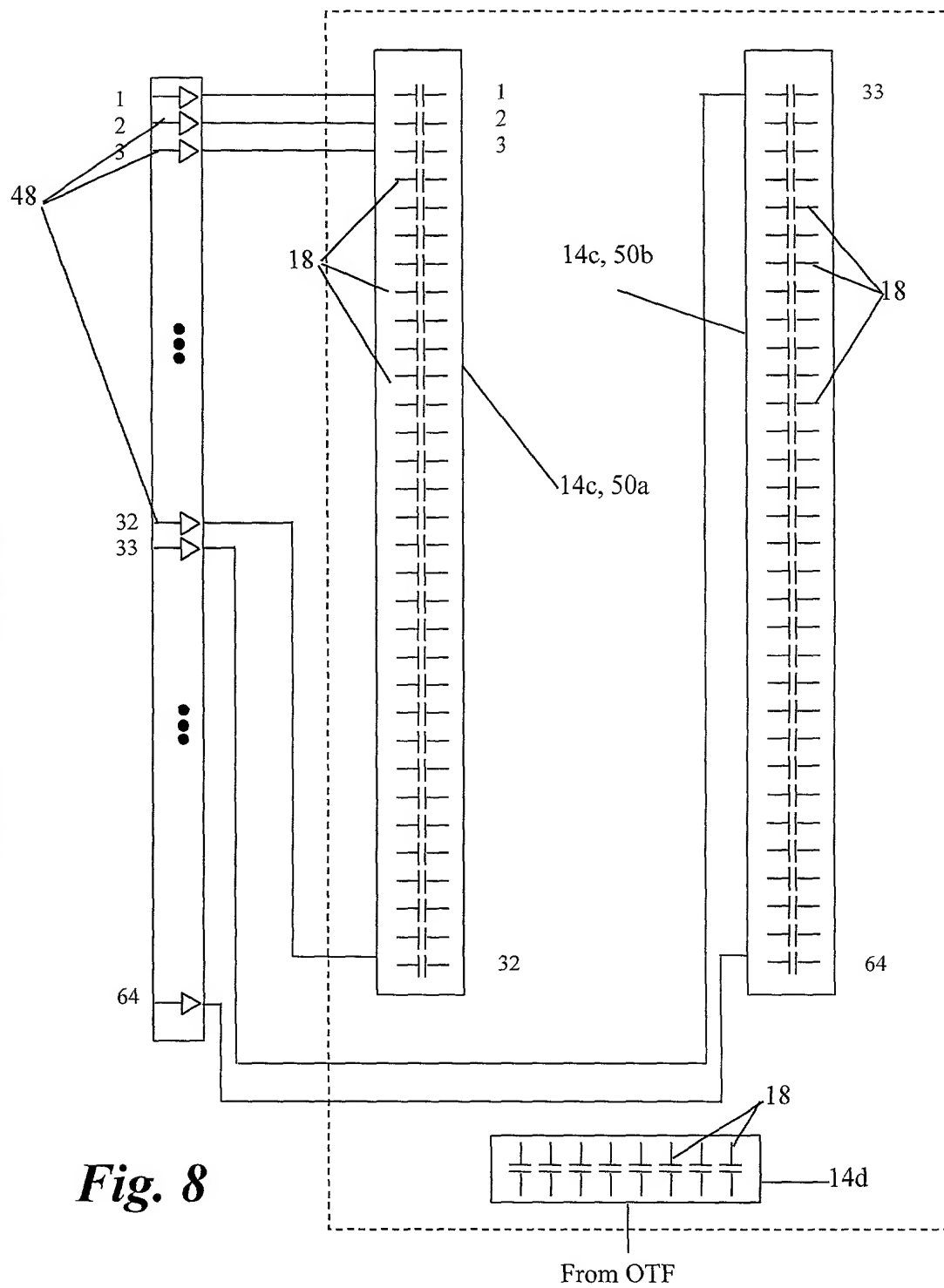
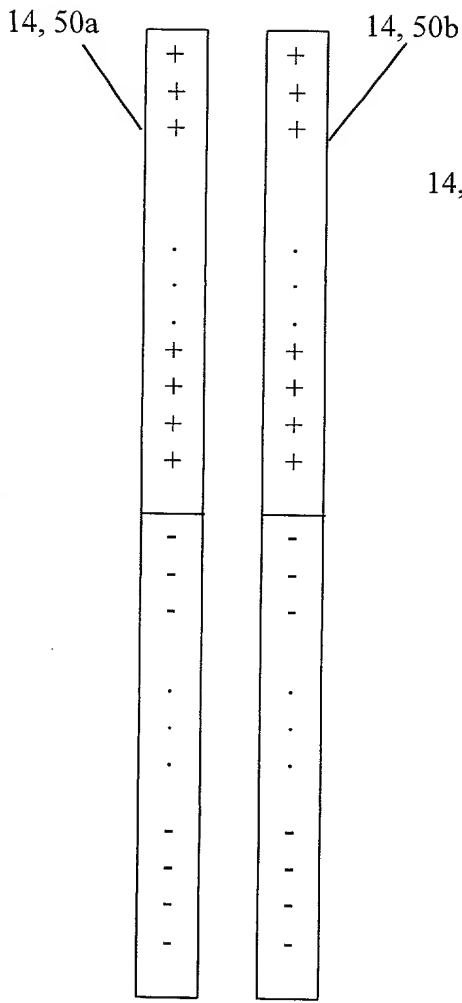
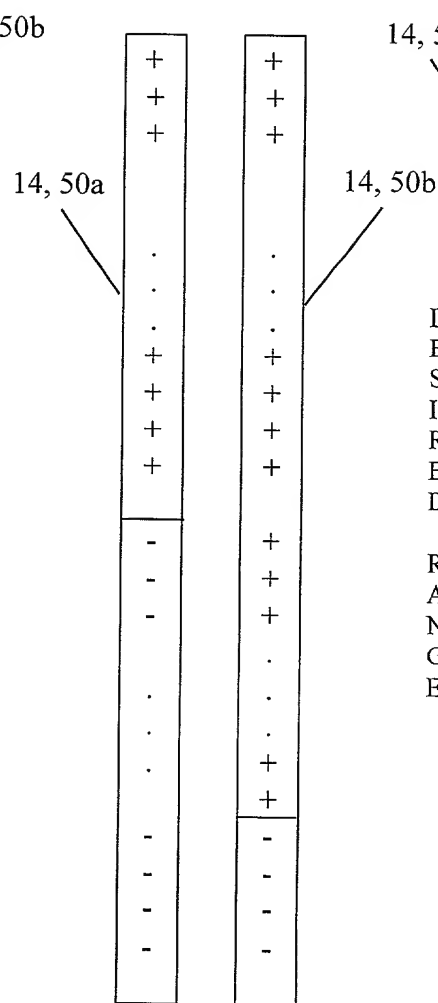


Fig. 8



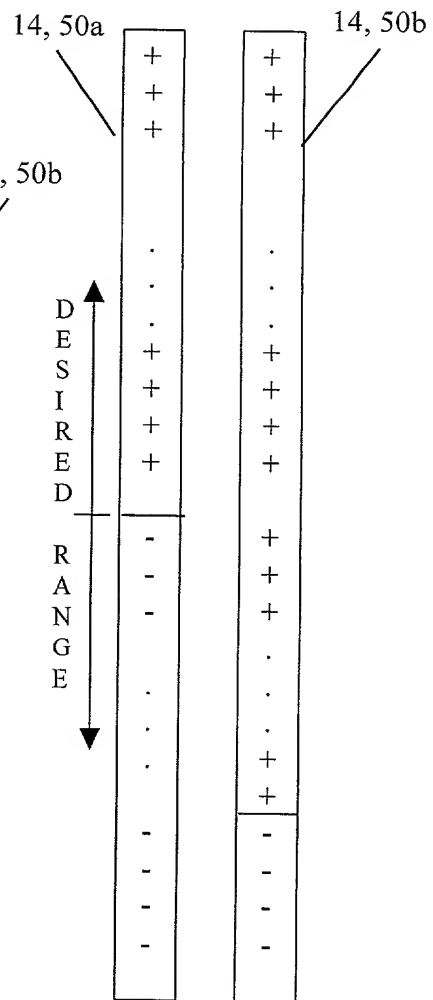
Initial State

Fig. 9a



Channel Tuning

Fig. 9b



Modulation and Drift

Fig. 9c

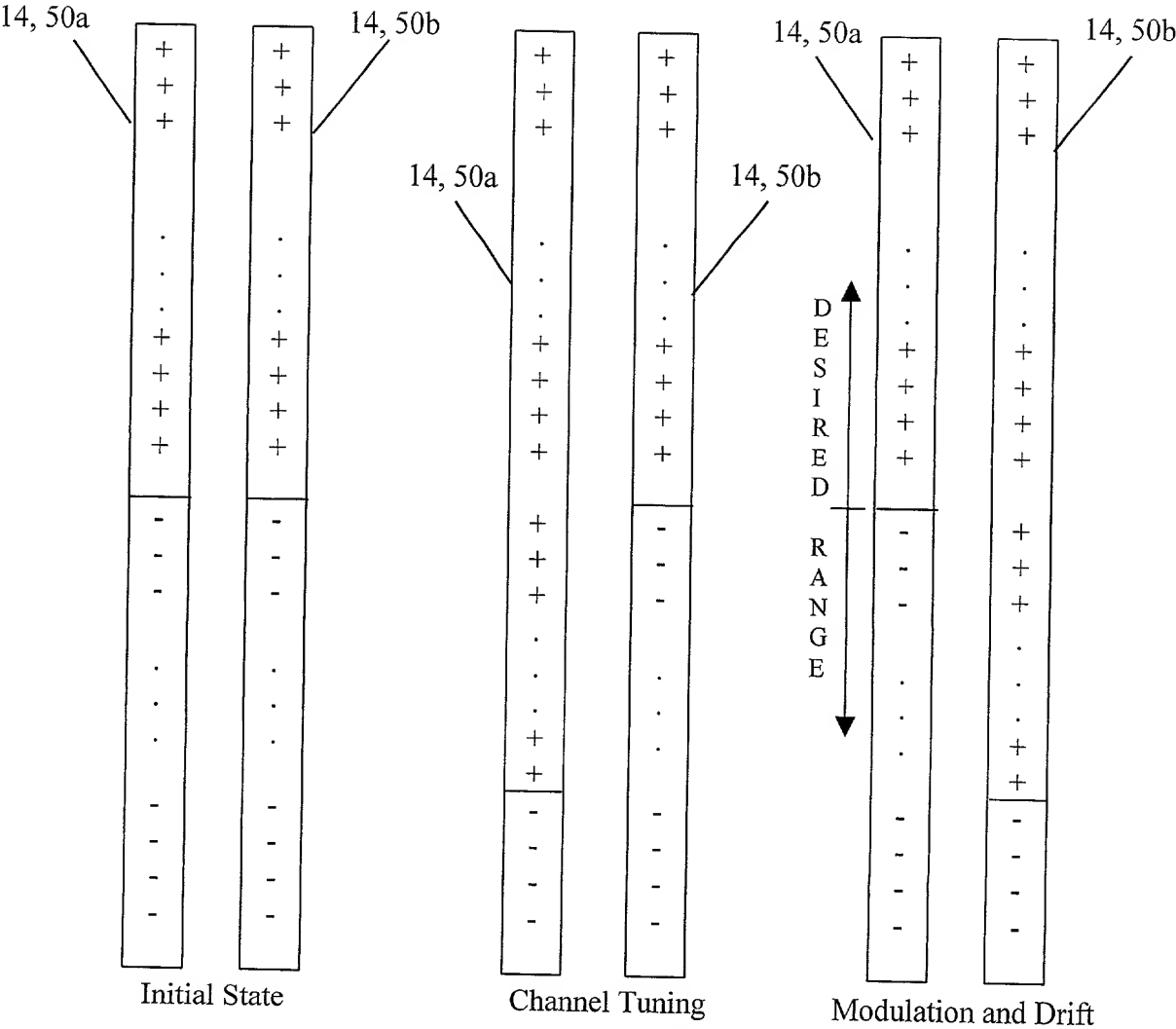


Fig. 10a

Fig. 10b

Fig. 10c

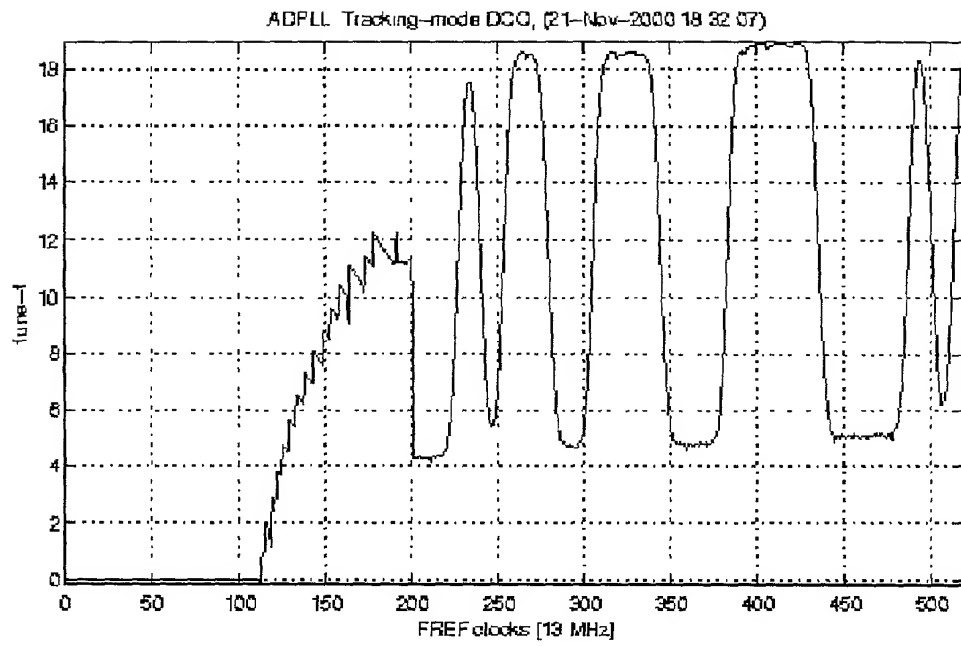


Fig. 11a

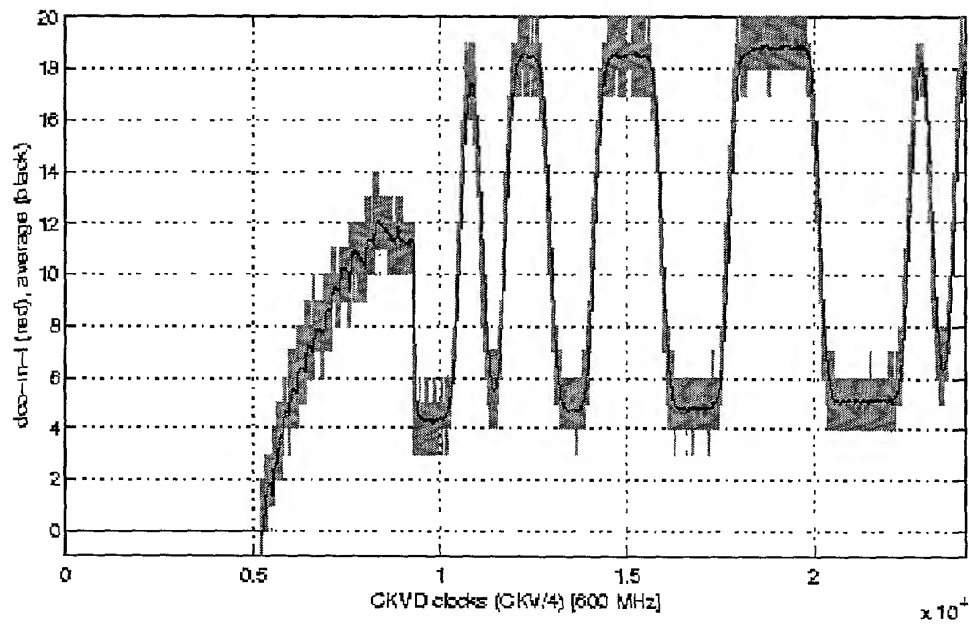
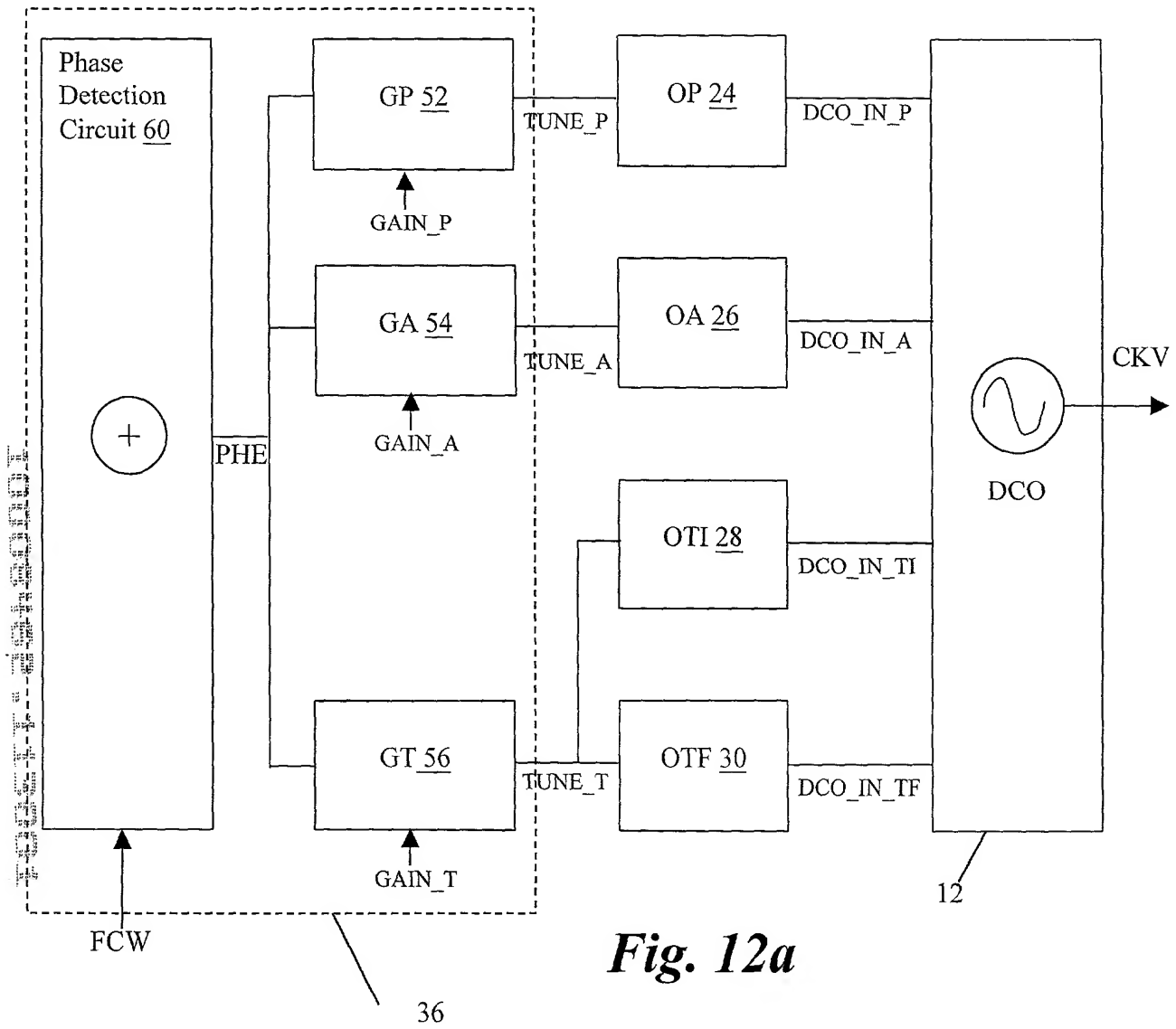
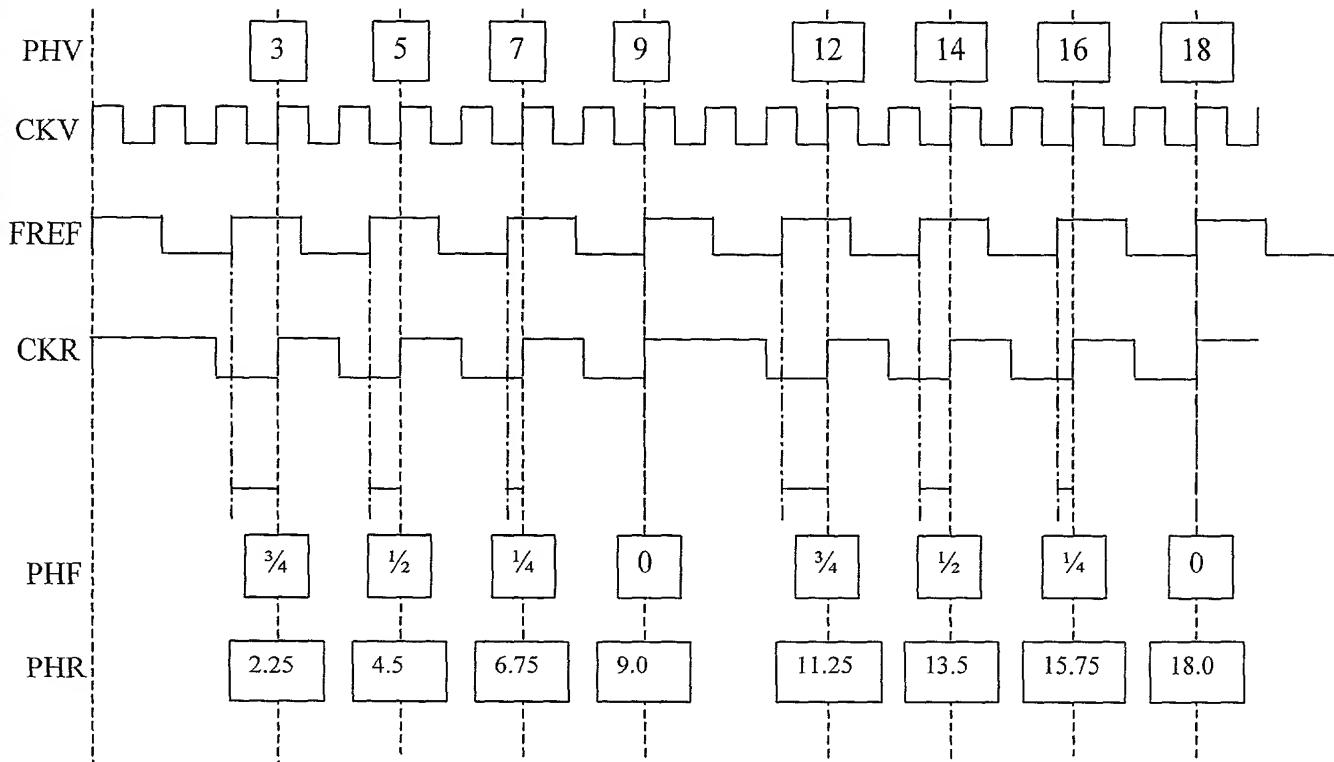
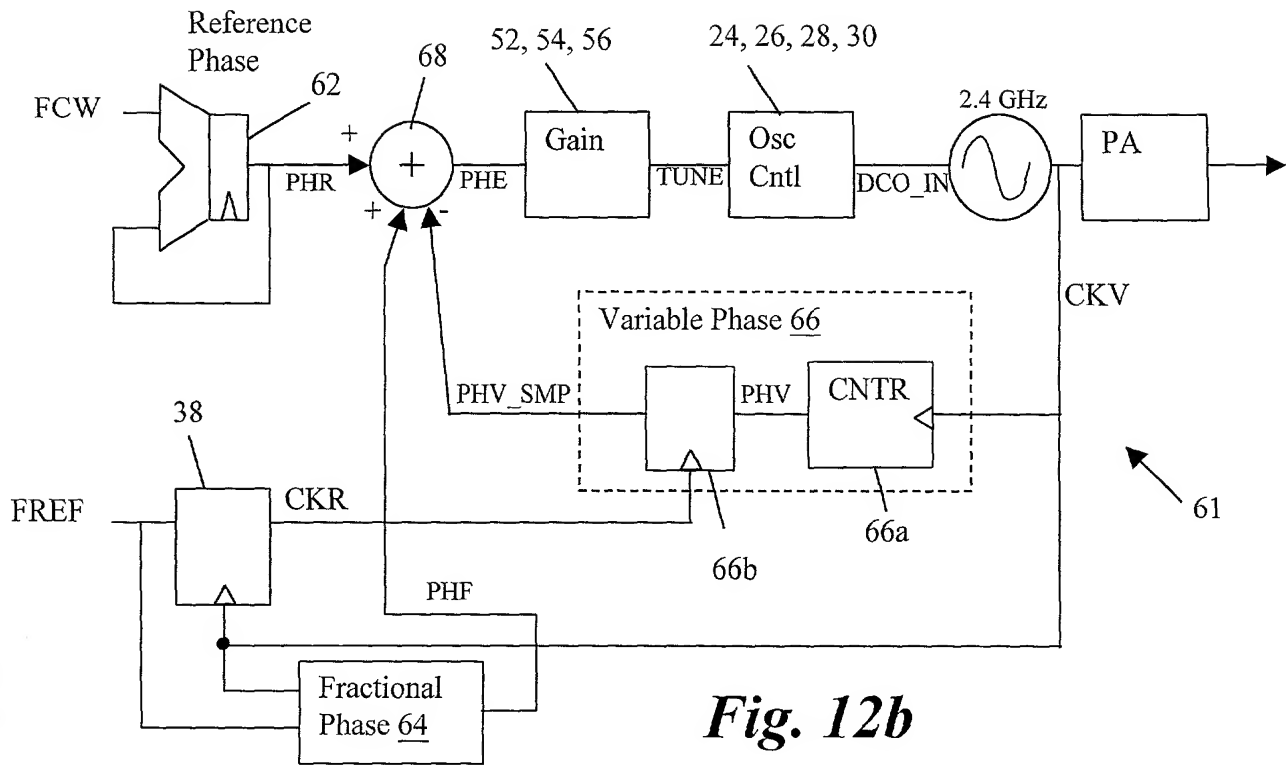
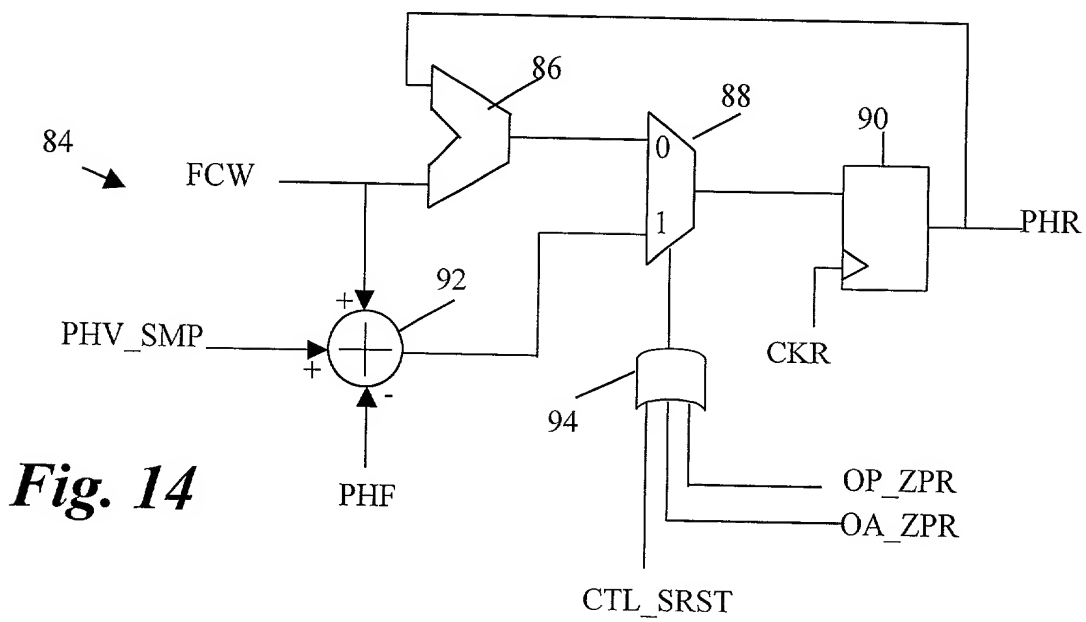


Fig. 11b







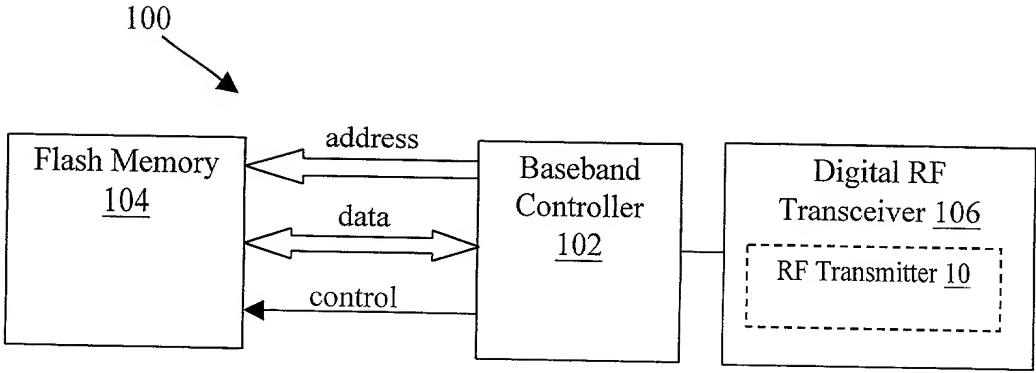


Fig. 15